

S&H Form: (2/01) DOCKET NO. 122.1561

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Naoki KUWATA, et al.

Serial No: 10/642,519

Group Art Unit: 2631

Confirmation No. 1583

Filed: August 18, 2003

Examiner:

For:

TIMING EXTRACTION CIRCUIT FOR USE IN OPTICAL RECEIVER THAT USES CLOCK OF FREQUENCY EQUAL TO ONE HALF OF DATA TRANSMISSION RATE,

AND DUTY CYCLE DEVIATION HANDLING CIRCUIT FOR USE IN OPTICAL

TRANSMITTER AND RECEIVER

REQUEST FOR CORRECTED FILING RECEIPT

Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

Sir:

It is requested that the assignee's address on the Official Filing Receipt be corrected. The correct assignee's address is **Fujitsu Limited**, **Kawasaki**, **Japan**, as is evidenced by the Copy of Assignment and Declaration, attached to the application as filed. For the convenience of the Patent and Trademark Office, attached is a photocopy of the original receipt on which the errors have been noted in red.

It is requested that a corrected Official Filing Receipt be issued in this application.

Respectfully submitted,

STAAS & HALSEY LLP

Date:

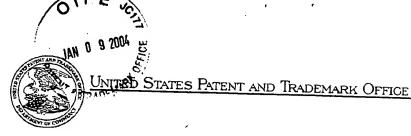
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FILING OR 371 APPL NO. ART UNIT FIL FEE REC'D (c) DATE ATTY.DOCKET NO DRAWINGS TOT CLMS IND CLMS 10/642,519 08/18/2003 2631 900 122,1561 33 8

21171 STAAS & HALSEY LLP SUITE 700 1201 NEW YORK AVENUE, N.W. WASHINGTON, DC 20005

CONFIRMATION NO. 1583 UPDATED FILING RECEIPT OC000000011537737*

Date Mailed: 12/19/2003

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Naoki Kuwata, Kawasaki, JAPAN; Takuji Yamamoto, Kawasaki, JAPAN;

Assignment For Published Patent Application

FUJITSU LIMITED:] -- Fujitsu Limited, Kawasari, Lapan

Domestic Priority data as claimed by applicant

This application is a CON of PCT/JP01/01139 02/16/2001

Foreign Applications

If Required, Foreign Filing License Granted: 11/11/2003

Projected Publication Date: 04/01/2004

Non-Publication Request: No

Early Publication Request: No

Title

Timing extraction circuit for use in optical receiver that uses clock of frequency equal to one half

of data transmission rate, and duty cycle deviation handling circuit for use in optical transmitter and receiver

Preliminary Class

375

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